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07-07-03

Docket No.: 509622000800  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Terry M. TURPIN, et al

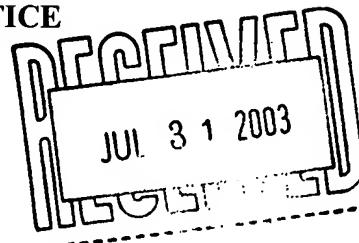
Application No.: 10/051,334

Group Art Unit: 2874

Filed: January 22, 2002

Examiner: Not Yet Assigned

For: WAVELENGTH DIVISION  
MULTIPLEXING ADD-DROP  
MULTIPLEXER USING AN OPTICAL  
TAPPED DELAY LINE



**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

In accordance with 37 CFR 1.97, Applicant(s) hereby make of record the following additional documents. A PTO Form SB/08 and a full copy of each of these documents accompany this statement.

Applicant(s) have become aware of the following documents, cited in an International Search Report issued June 25, 2003, during the prosecution of international application no. PCT/US02/01561, which corresponds to the above referenced application, and in accordance with 37 CFR 1.97(c) and (e)(1) or (b)(3), hereby submit(s) these documents for the Examiner's consideration. These documents are cited on the enclosed PTO Form SB/08, and a copy of the International Search Report and each document cited thereon are enclosed as well.

This statement is not to be interpreted as a representation that the cited documents are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any document herein be construed *per se* as a representation that such

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document is prior art. Moreover, Applicant(s) understand(s) the Examiner will make an independent evaluation of the cited documents.

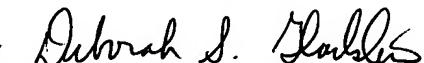
The undersigned hereby certifies that each item contained in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

This Supplemental Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits; accordingly, no fee is required.

In the event that the Patent and Trademark Office determines that an extension and/or other relief is required, Applicants petition for any required relief including extensions of time and authorize the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952** referencing **509622000800**. However, the Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: July 17, 2003

Respectfully submitted,

By   
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Form PTO-1449



**INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION**

(Use several sheets if necessary)

|                              |                               |
|------------------------------|-------------------------------|
| Docket Number 509622000800   | Application Number 10/051,334 |
| Applicant                    | Terry M. TURPIN et al.        |
| Filing Date January 22, 2002 | Group Art Unit 2874           |
| Mailing Date July 17, 2003   |                               |

**U.S. PATENT DOCUMENTS**

| Examiner Initials | Ref. No. | Date | Document No. | Name | Class | Subclass | Filing Date If Appropriate |
|-------------------|----------|------|--------------|------|-------|----------|----------------------------|
|                   |          |      |              |      |       |          |                            |

**FOREIGN PATENT DOCUMENTS**

| Examiner Initials | Ref. No. | Date      | Document No. | Country | Class | Subclass | Translation YES NO |
|-------------------|----------|-----------|--------------|---------|-------|----------|--------------------|
|                   | 1.       | 7/29/1999 | WO 99/38348  | PCT     |       |          | X                  |

**OTHER DOCUMENTS**

(including author, title, Date, Pertinent Pages, Etc.)

| Examiner Initials | Ref. No. | Title   |
|-------------------|----------|---|
|                   | 2.       | Yoon et al. "A Tunable Optical Add/Drop Multiplexer Using a Fiber-Optic Tapped Delay-Line Transversal Filter" IEEE 8/1999, pp. 785-786. |
|                   | 3.       | Shirasaki "Virtually Imaged Phased Array" Fujitsu Sci. Tech. Journal Vol. 35, No. 1, 7/1999, pp. 113-125.                               |

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| EXAMINER: | DATE CONSIDERED: |
|-----------|------------------|

EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

va- 38925

PTO/SB/08 (2-92)

Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE